

FIG. 1

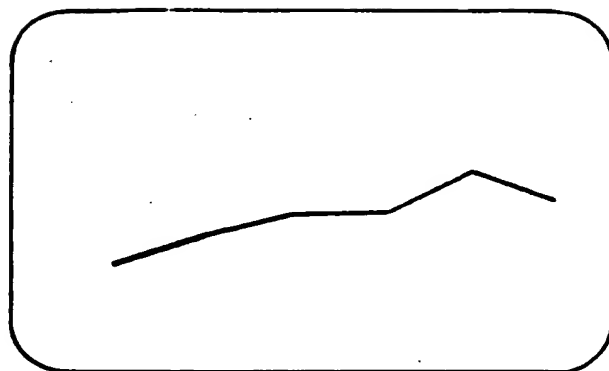


FIG. 1A

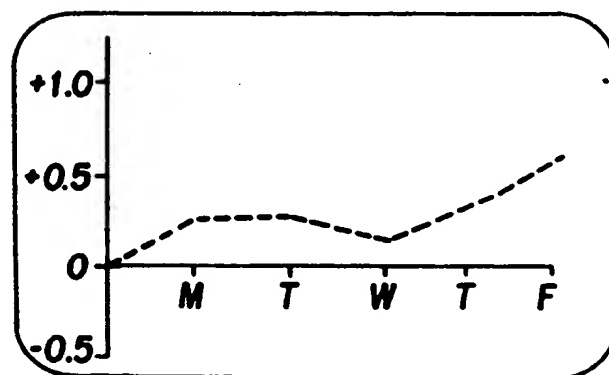


FIG. 1B

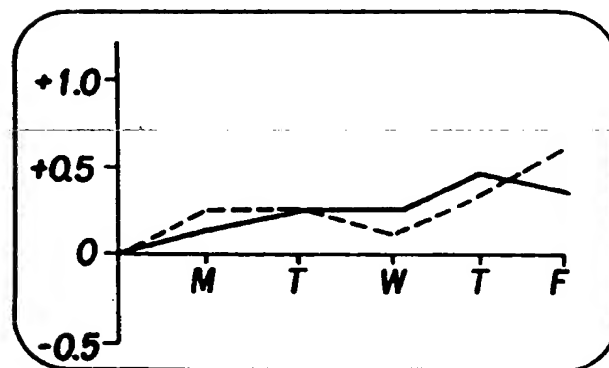


FIG. 1C

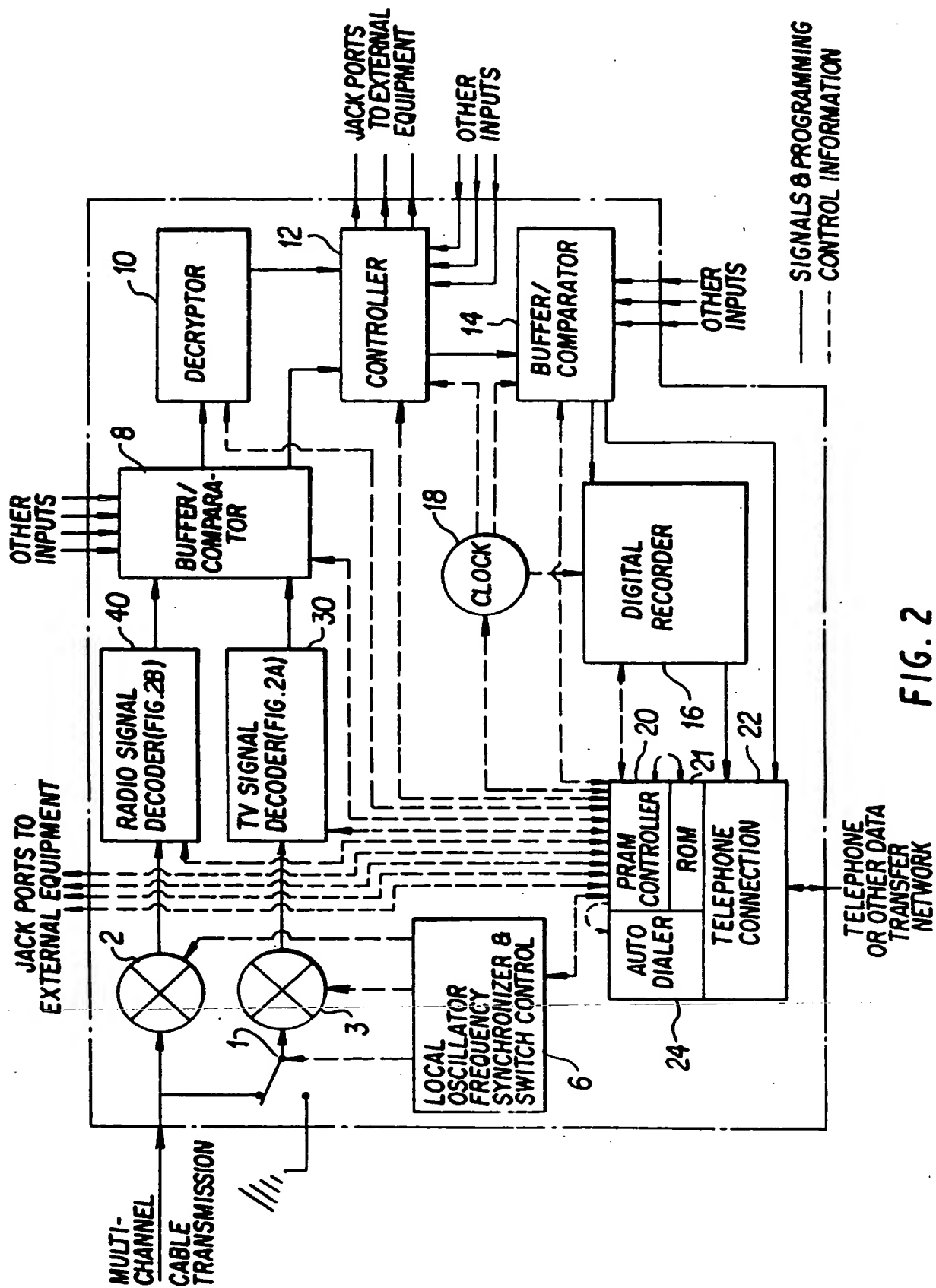


FIG. 2

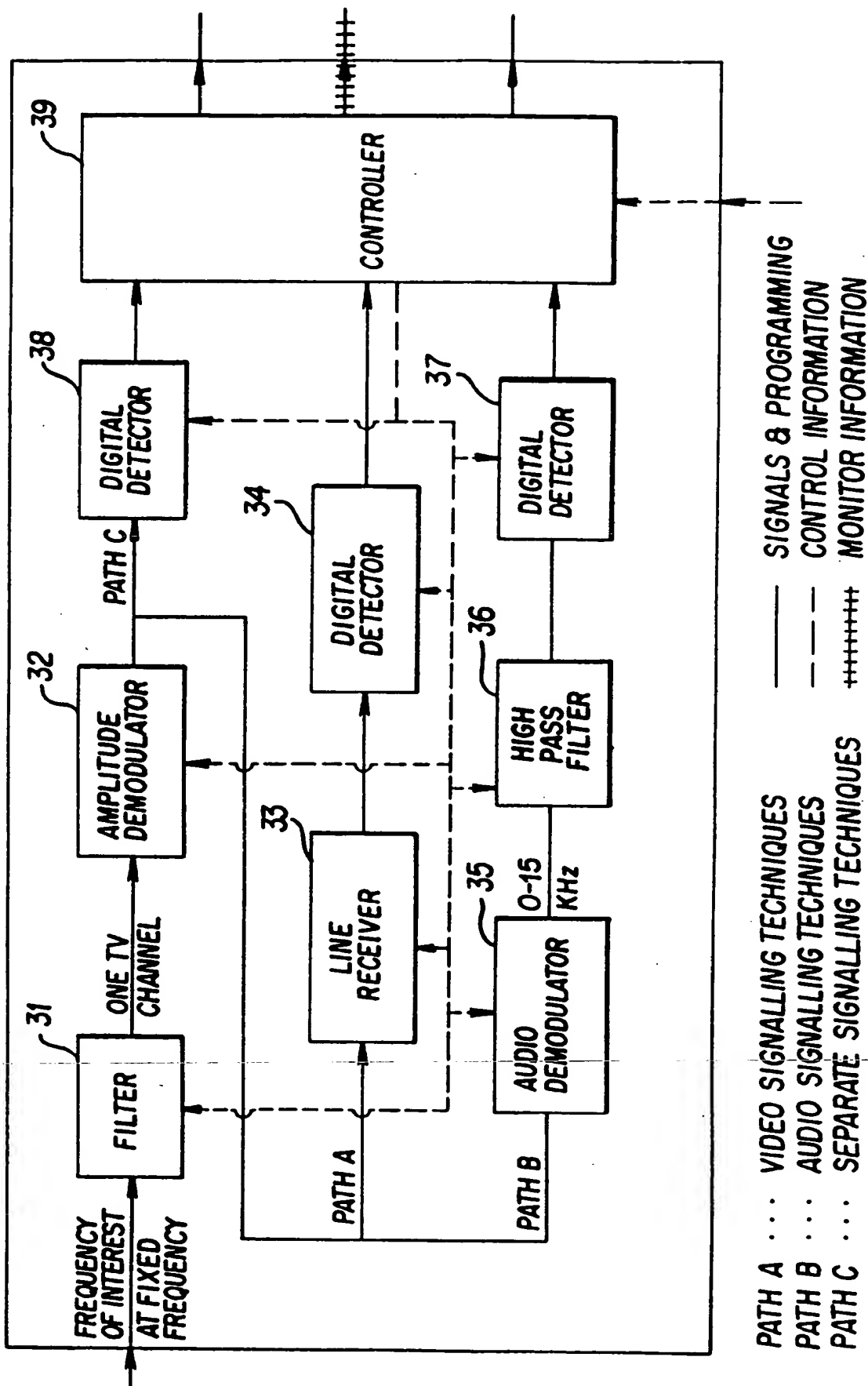


FIG. 2A

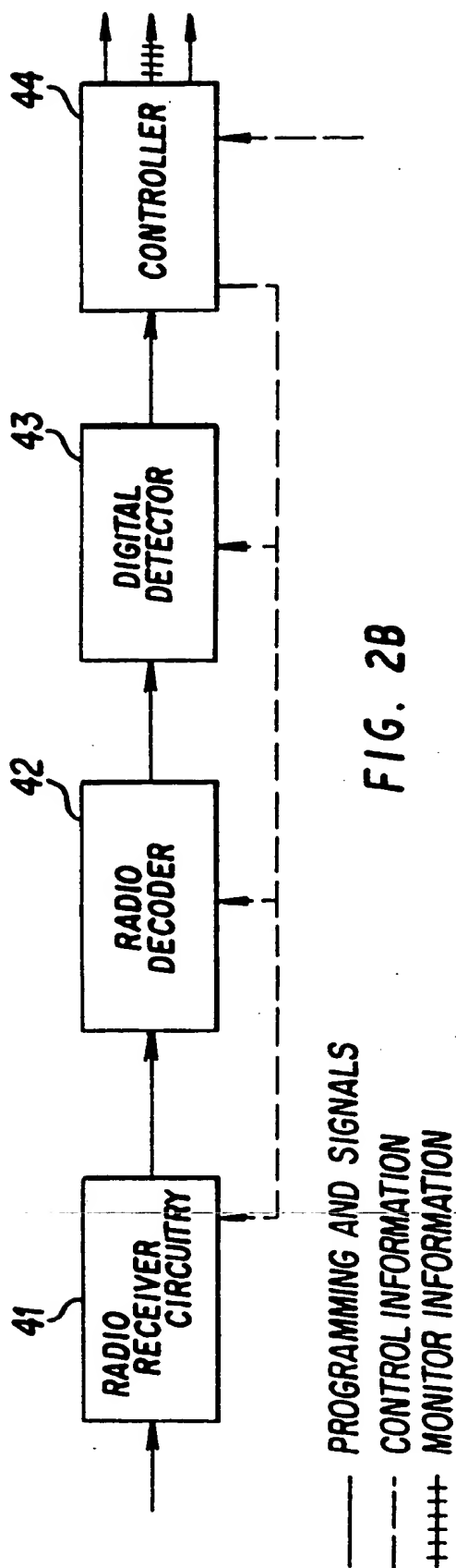


FIG. 2B

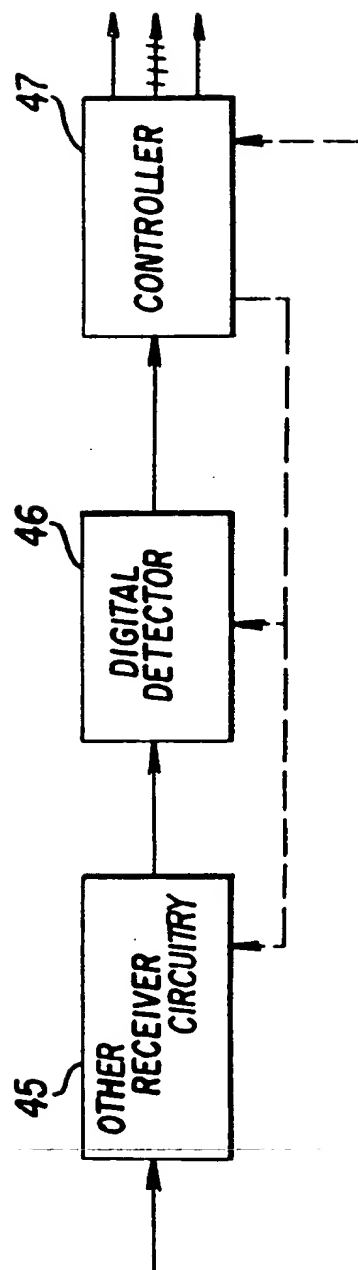
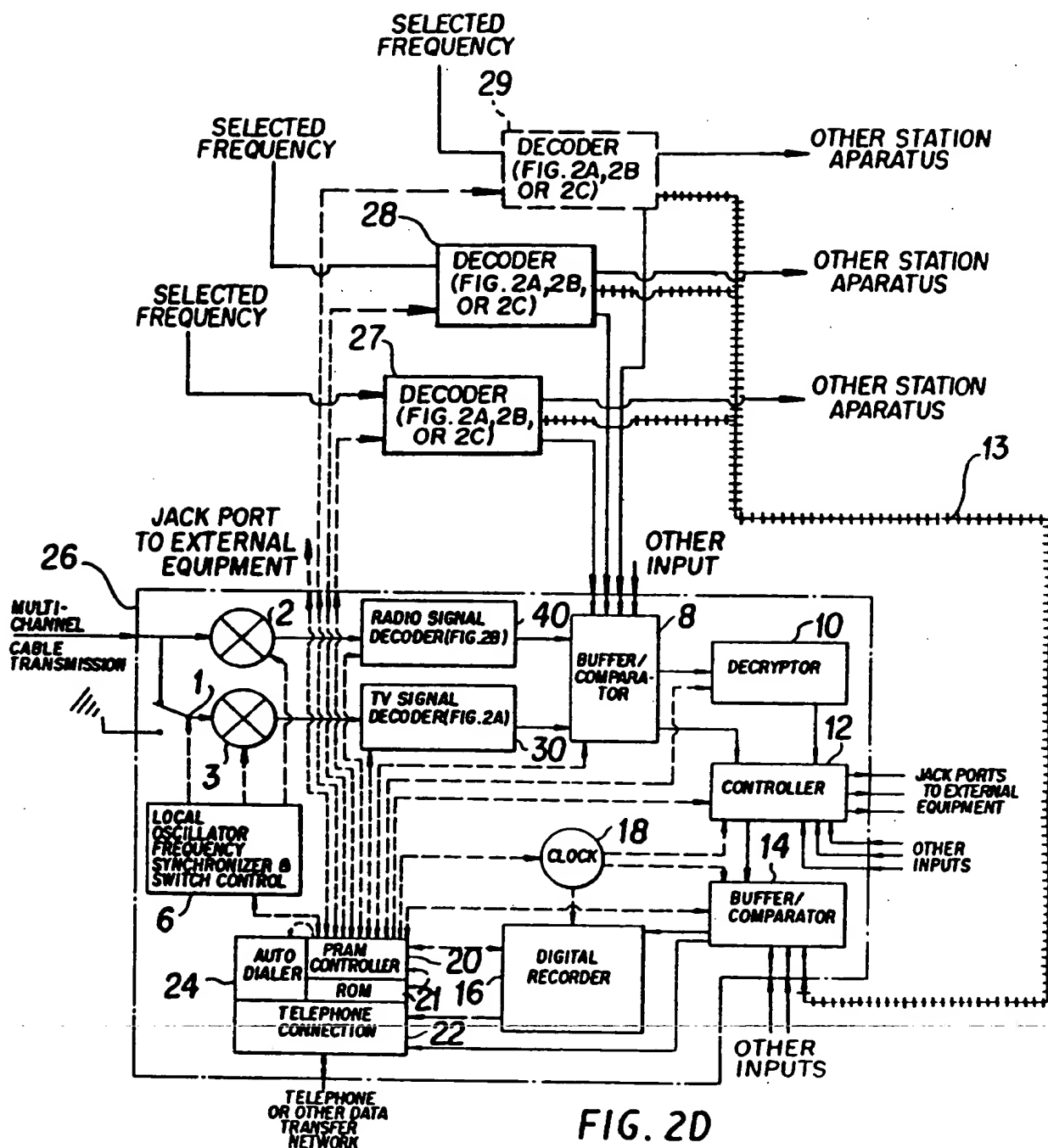


FIG. 2C



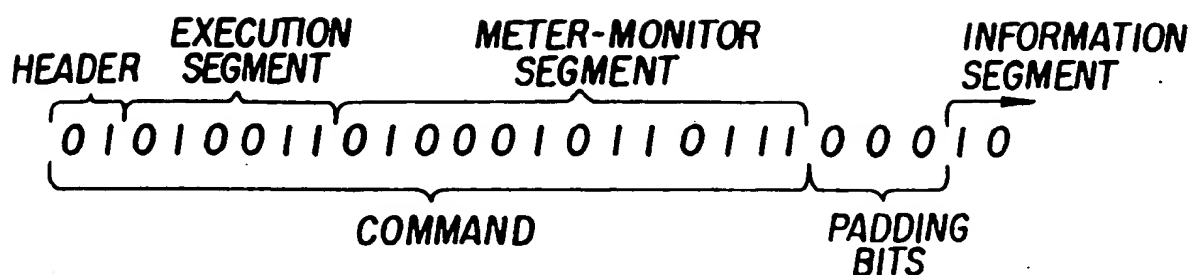


FIG. 2E

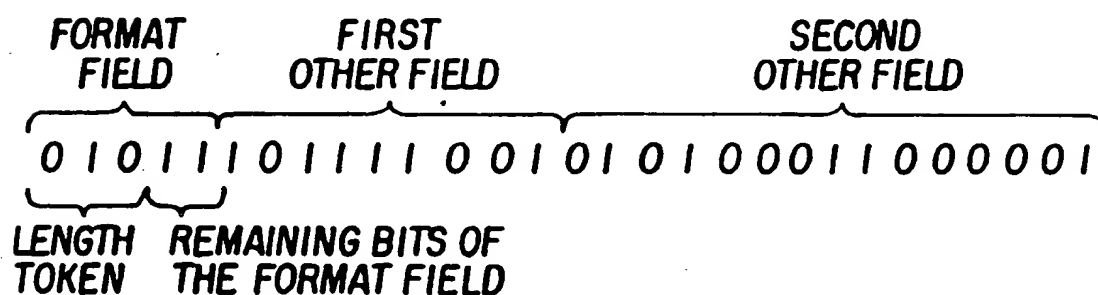


FIG. 2F

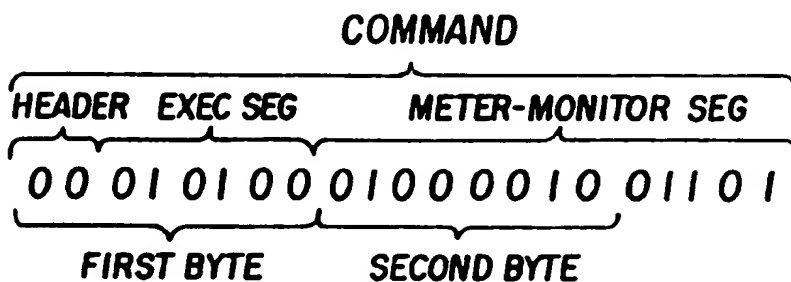


FIG. 2G

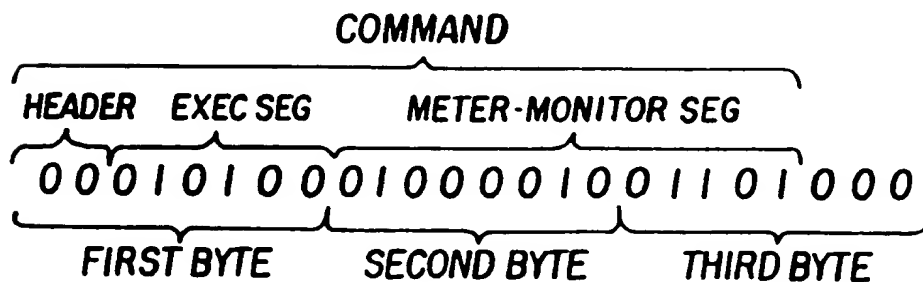


FIG. 2H

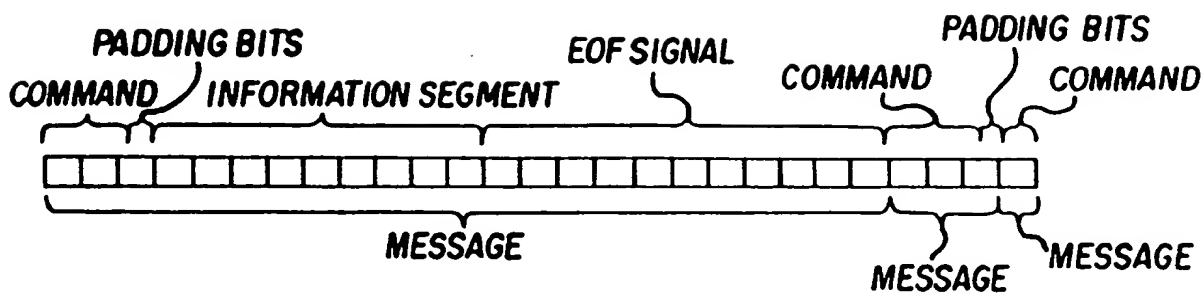


FIG. 2I

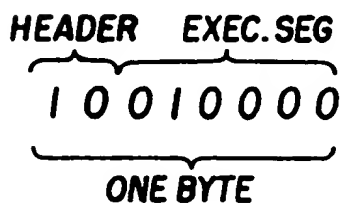


FIG. 2J

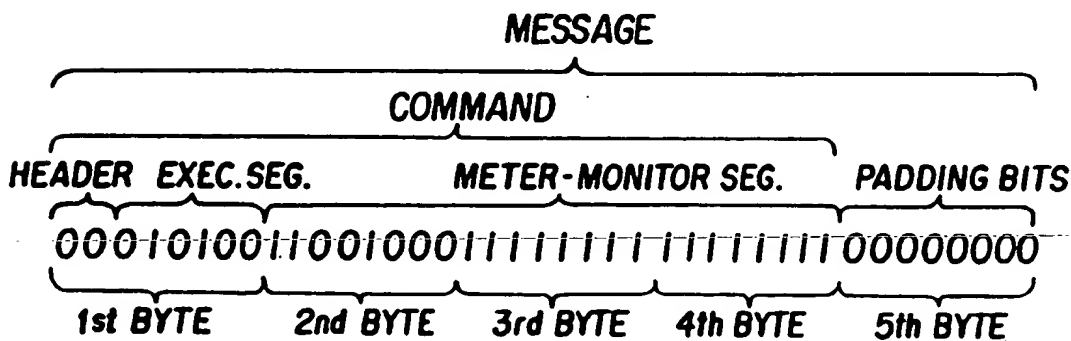


FIG. 2K

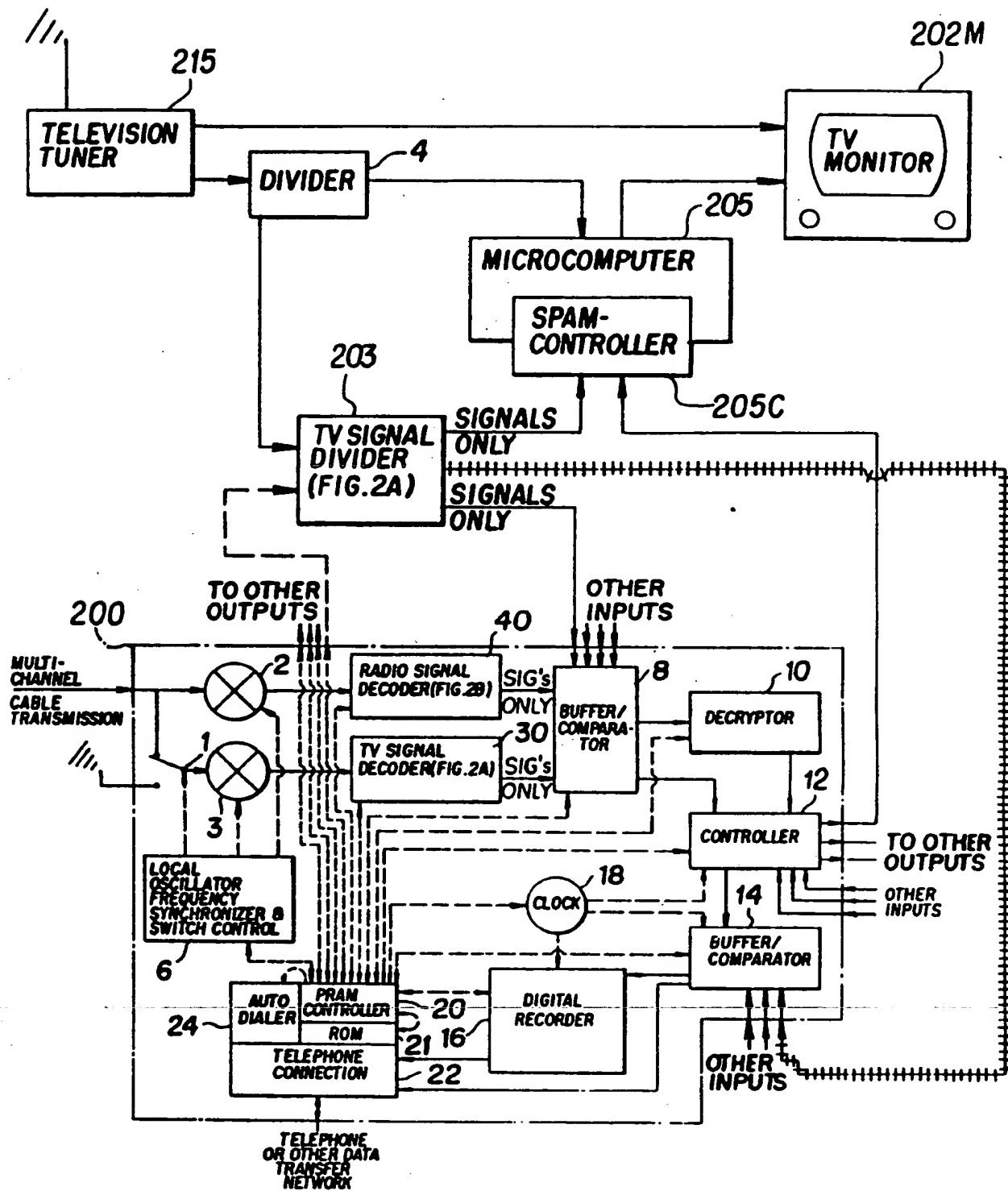


FIG. 3

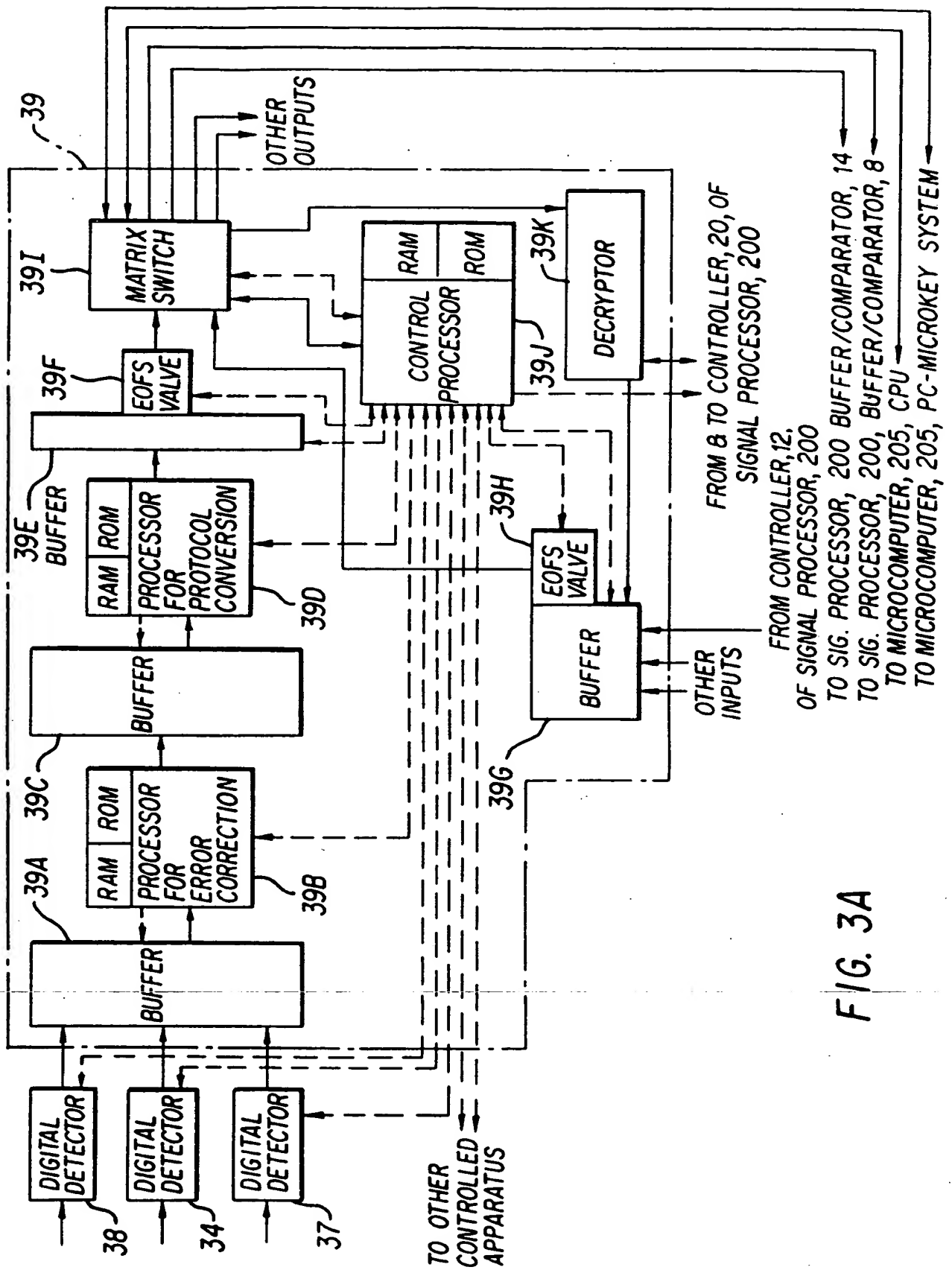


FIG. 3A

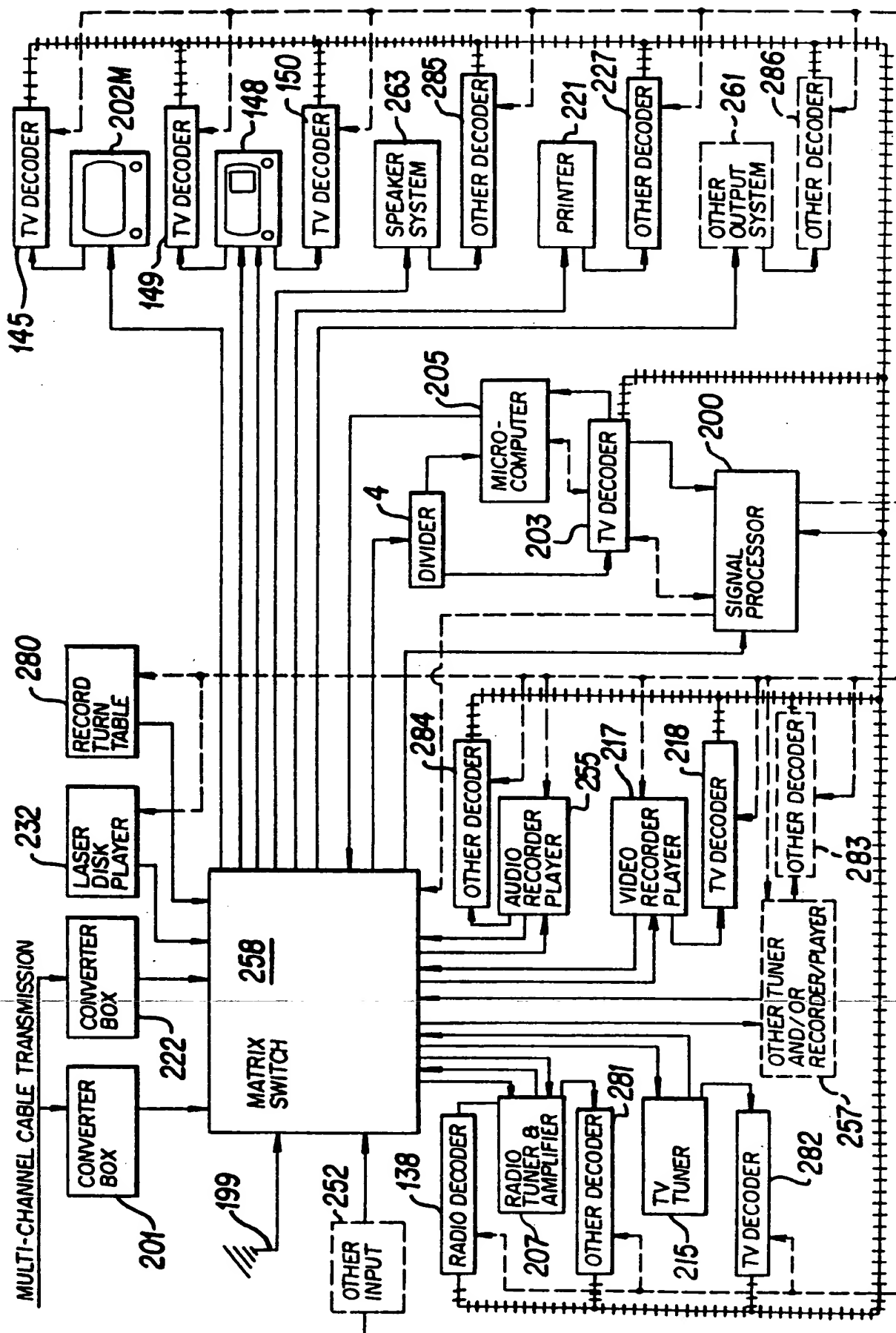


FIG. 5

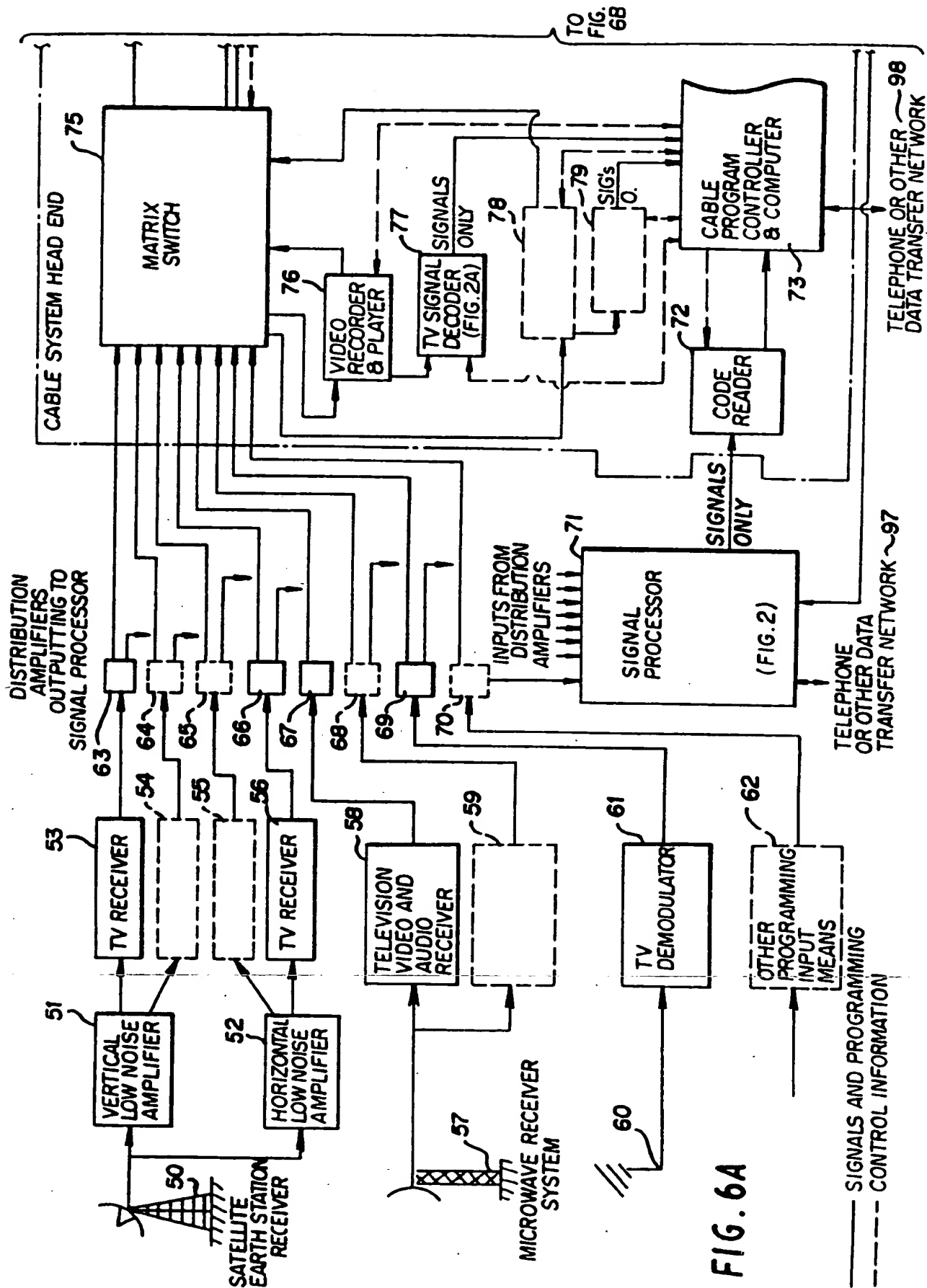


FIG. 6A

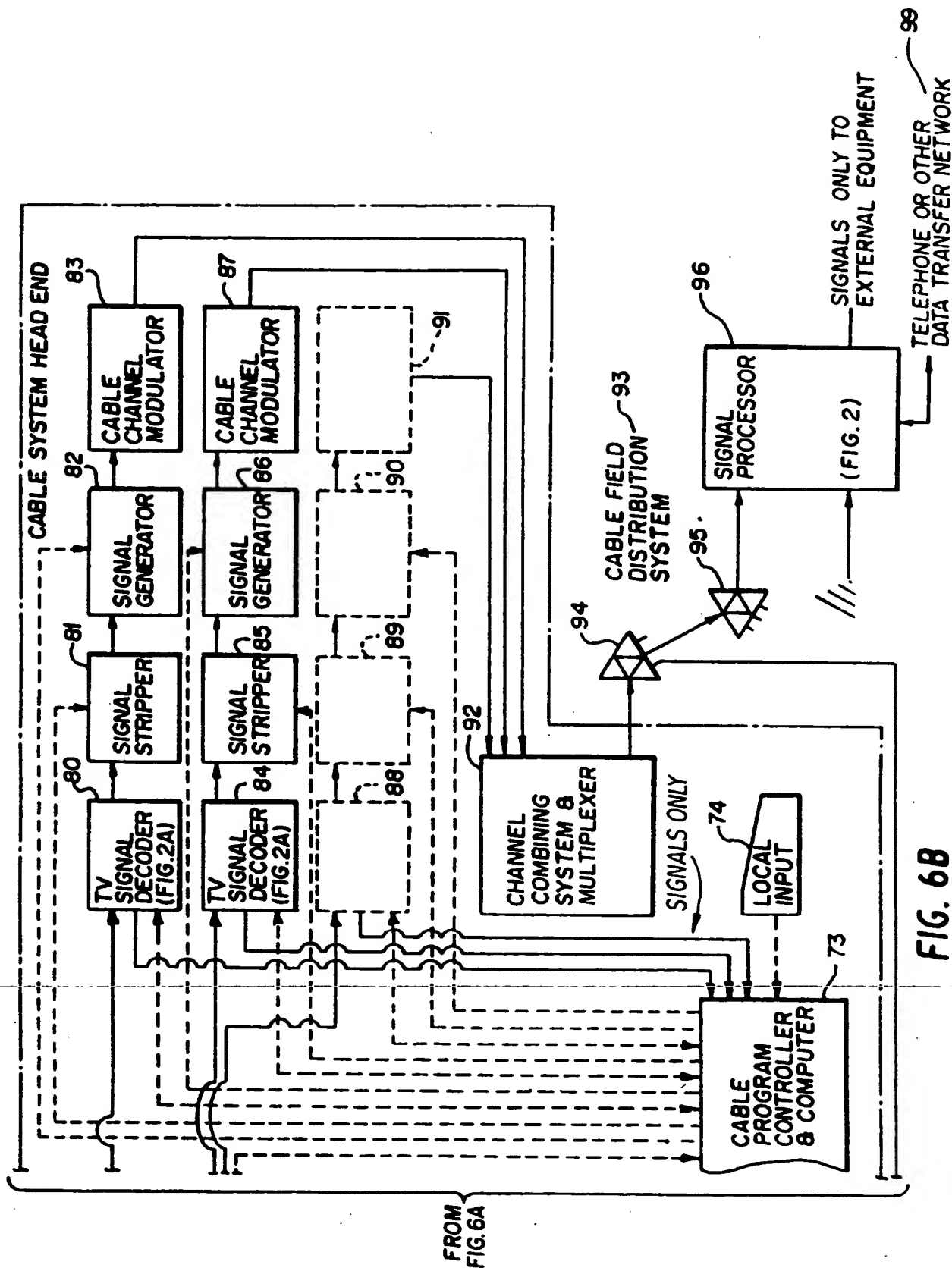


FIG. 6B

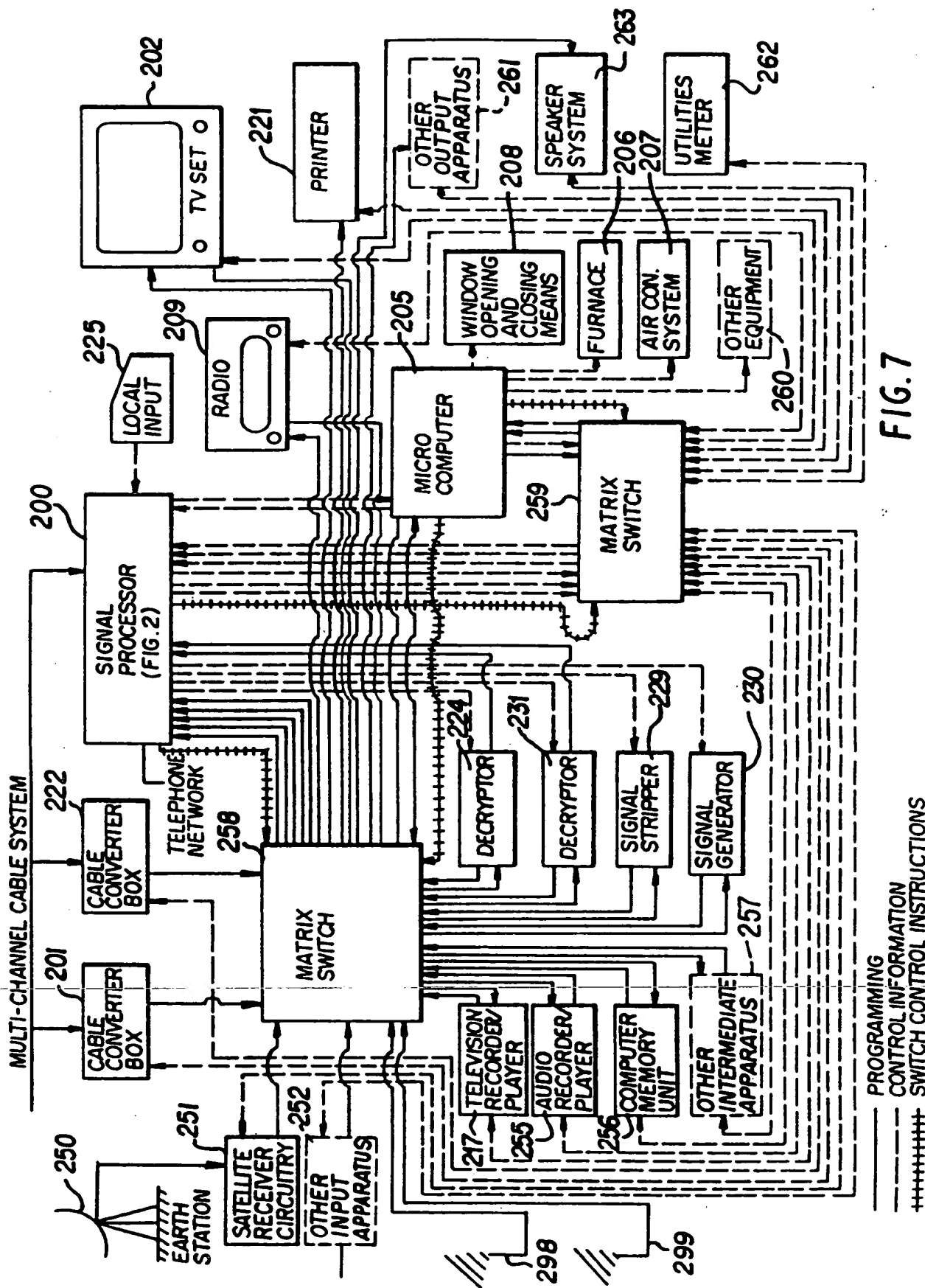


FIG. 7

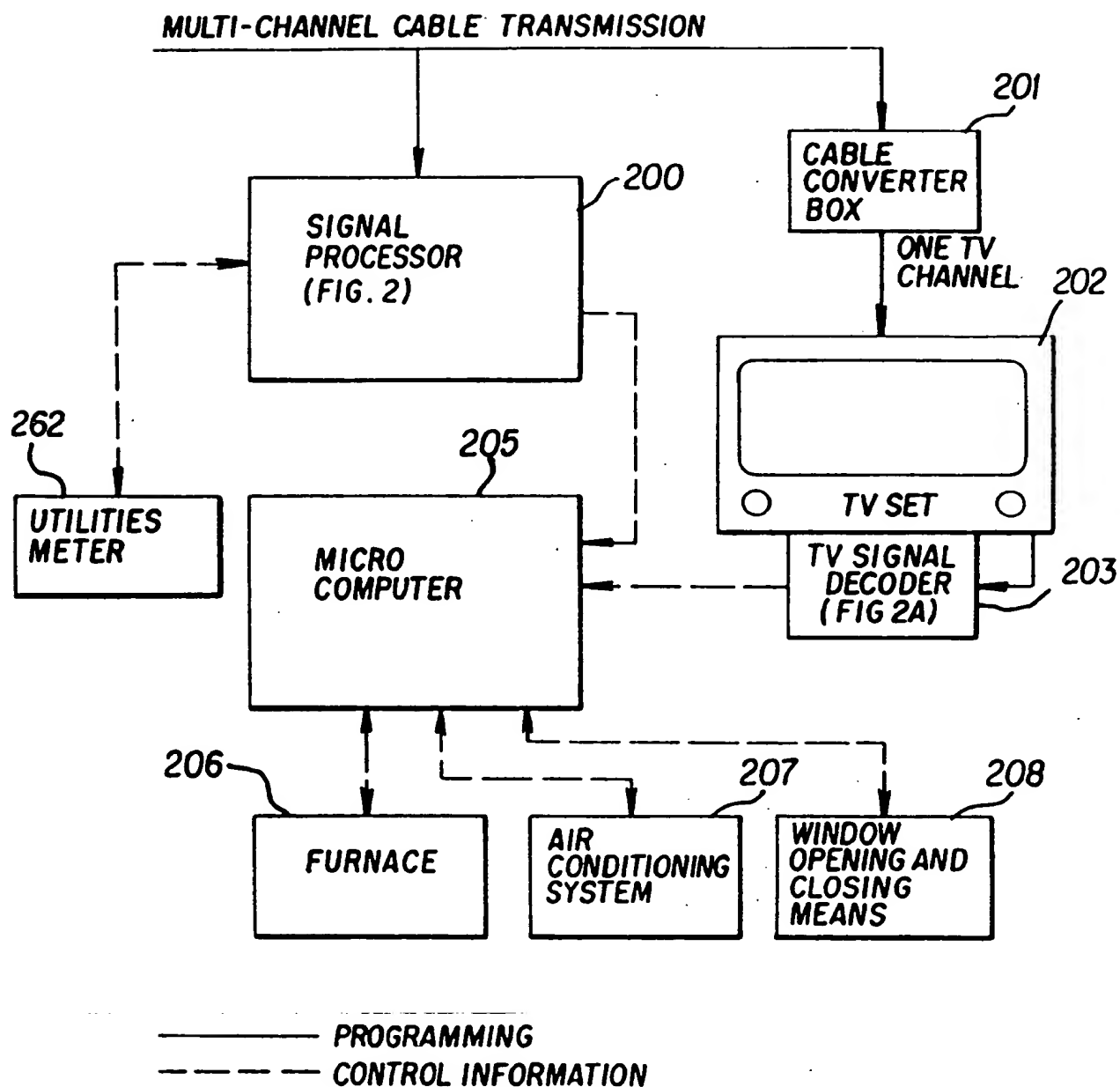


FIG. 7A

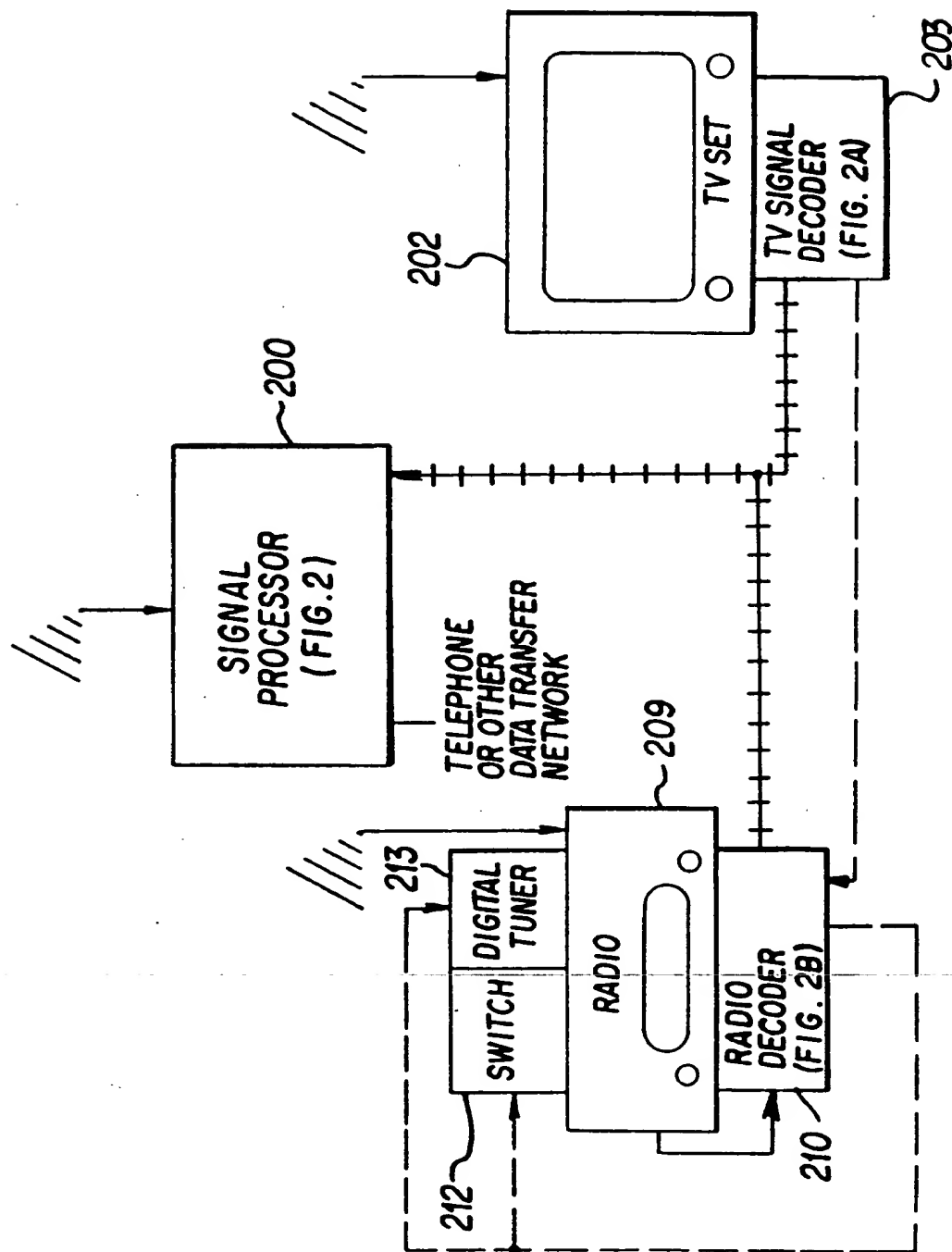


FIG. 7B

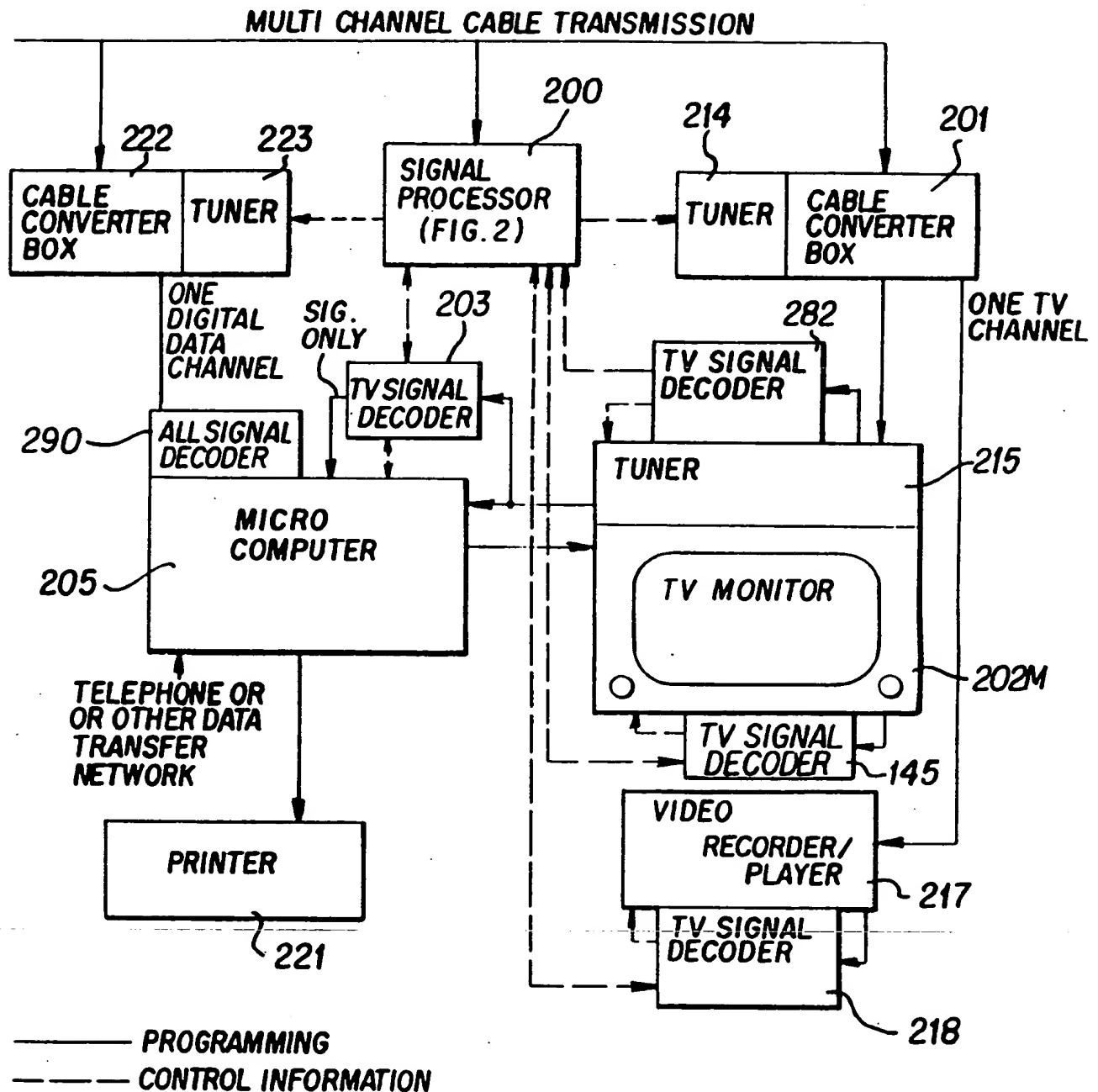


FIG. 7C

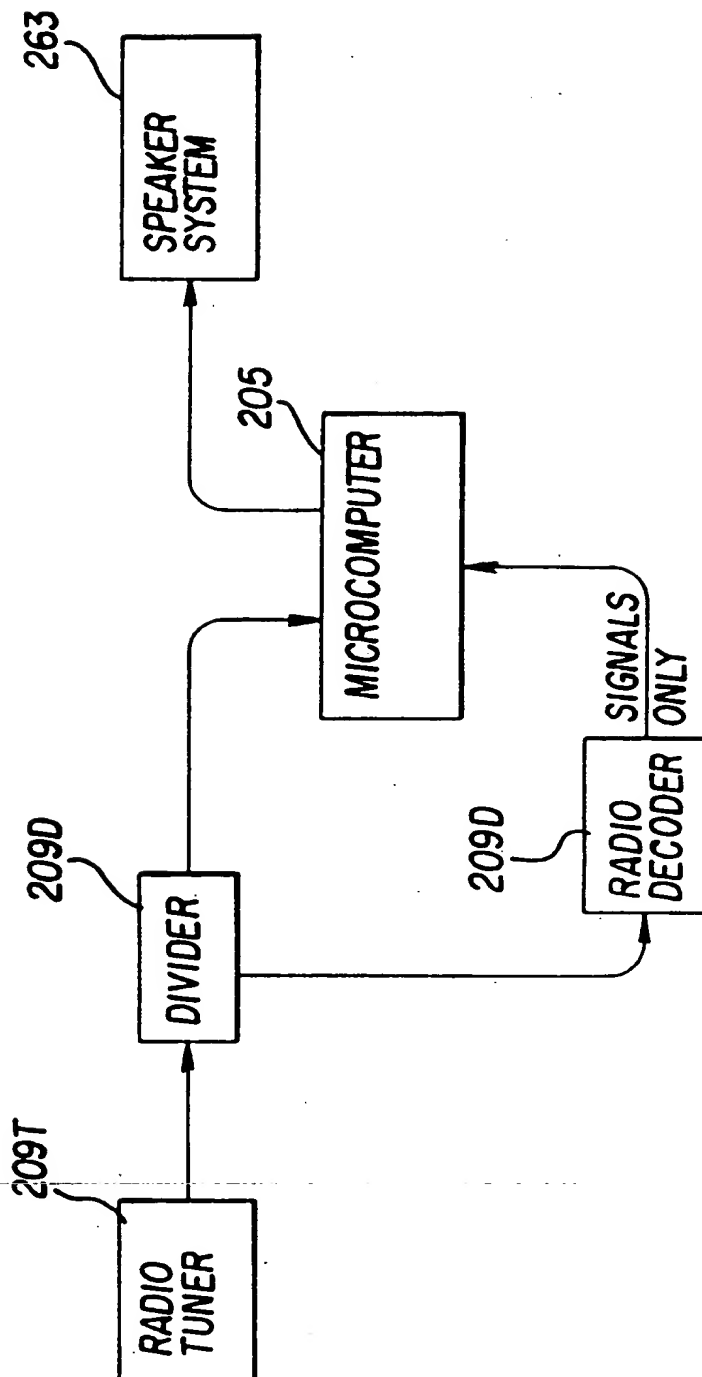


FIG. 7D

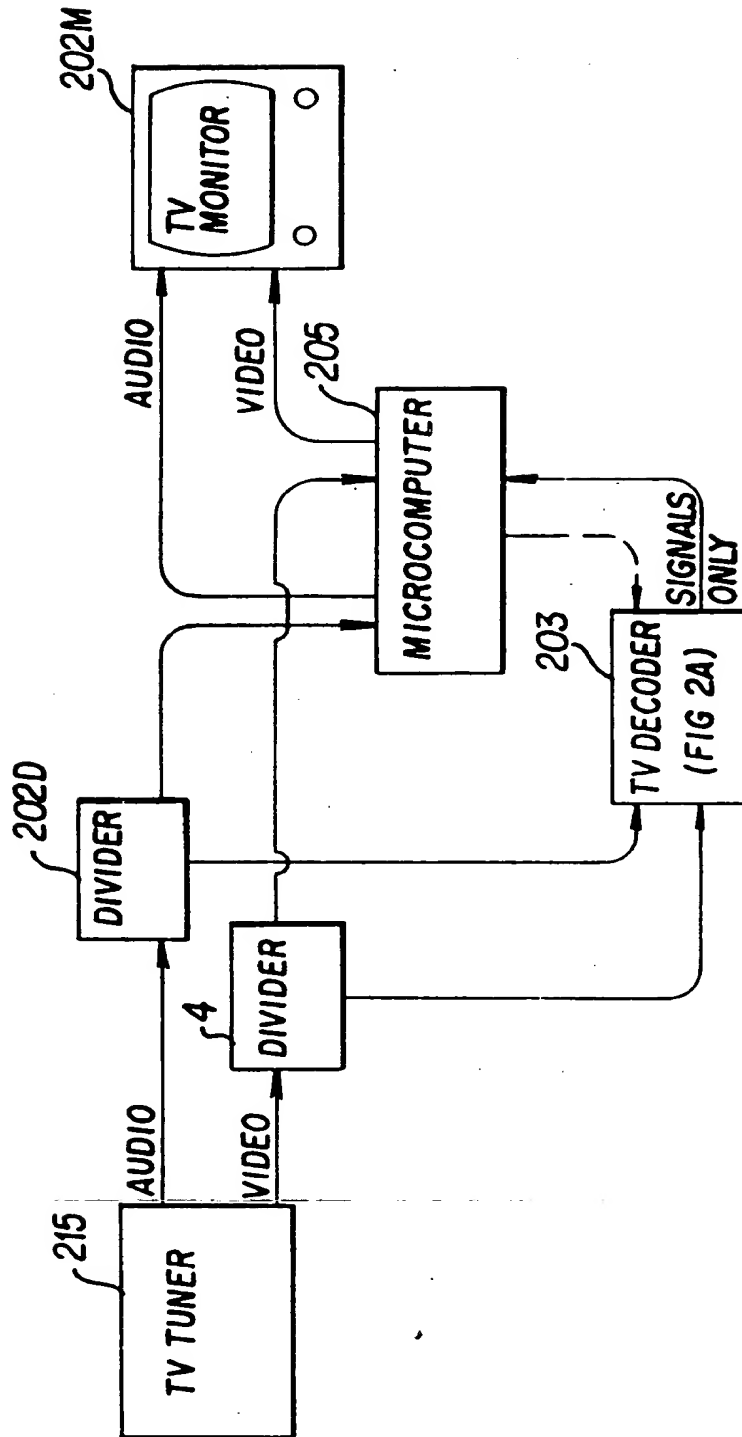
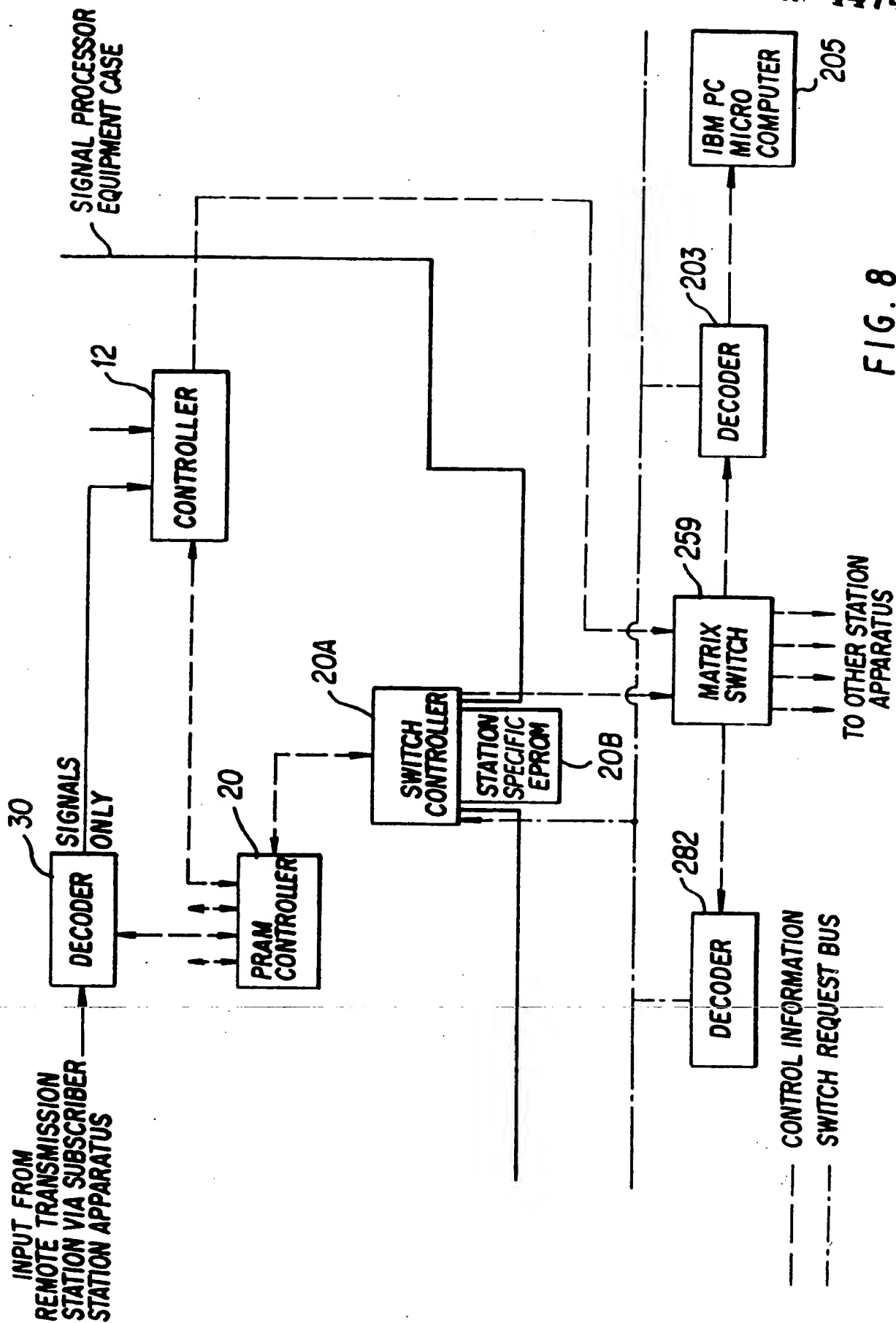


FIG. 7E



**INPUT FROM
REMOTE TRANSMISSION
STATION VIA SUBSCRIBER
STATION APPARATUS**

DECODER

SIGNALS ONLY

12

CONTROLLER

SIGNAL PROCESSOR EQUIPMENT CASE



PRAM
CONTROLLER

20

20A

SWITCH CONTROLLER

**STATION
SPECIFIC
EPROM**

208

282

DECODER

CONTROL INFORMATION SWITCH REQUEST BUS

259

MATRIX SWITCH

**TO OTHER STATION
APPARATUS**

203

DECODER

**IBM PC
MICRO
COMPUTER**

205